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Lecture on Sensor Networks

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Communication in sensor networks

Terms

Terms

Classification and Motivation

Static channel assignment

Dynamic channel assignment

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Classification

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Classification and Motivation: The (ISO/OSI-network-) layer model

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Classification and Motivation

Conventional wired computer networks consist of special routers which are connected by dedicated channels. There is only one router at the beginning and end of a (virtual) channel. Both of them use the medium exclusively. Hence, gaining access to the medium causes no problems. Instead, a way through the graph of connected routers has to be chosen.

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Classification and Motivation

If many computers communicate through a shared channel, the assignment of the "right to talk" has to be organized. On the other hand there is no routing problem if everyone listens to the same channel.

Example:

In discussions, every participant should have an exclusive right to talk. The negotiation about the right to talk often takes place via the "visual channel". The intention to talk is signaled to the discussion leader who then assigns the right to talk. Yet access control to the medium does not always work very well.

In the context of mobile ad-hoc networks the shared medium often is an entire radio band (e. g. the ISM band on 2,4 GHz). However, many sensor nodes use only a single frequency or infrared signals.

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Static channel assignment

The channel is divided 1) into time slots 2) into frequency spectra

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Static channel assignment: Problems

Example: Classical telephony (mixed dynamic/static)

The assignment of wires (channels) to connection desires is dynamic – and can therefore fail. Once the connection is established, the required bandwidth is available for the duration of the conversation – this is guaranteed.

the entire time.

Example: Voice-over-IP (dynamic only)

Voice is split up into small packets and transmitted over the IP-based network. The packet only arrives (on time or at all) if enough bandwidth exists (mostly works due to sufficiently large bandwidth; so-called over provisioning). But guarantees cannot be given.

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Static channel assignment

assignment

Error control

Dynamic channel

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Dynamic channel assignment: Basic assumptions

The following channel assignment procedures assume these principles:

Subdivision of time (particularly important in sensor networks):

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Dynamic channel assignment: Basic assumptions

The following channel assignment procedures assume these principles:

Carrier detection:

In the case of our sensor nodes no error protection is implemented on level 2 of the hardware. Thus, we have to implement it in software. What follows is an excursus on error protection.

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Example: On average every $1,000$ th byte is disturbed. Packets also consist of 1,000 bytes. Does a repeated transmission of defective packets make sense here?

Error correcting codes: A large amount of redundancy is necessary. This makes sense if a) no feedback channel is available or b) a retransmission delays the packet considerably (e. g. telephony, video conferences).

The applied technique also depends on the typical error distribution, particularly on its variance. If errors occur in so-called bursts, hence very clustered, a repeated transmission of a packet may be the solution. Whereas statistically independent errors promote error correcting codes.

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Error control: The Hamming-Distance

Given two codewords:

The Hamming-Distance of two codewords corresponds exactly to the number of bits the codewords differ in. This is calculated using the XOR-operation. In other words: If two codewords have the Hamming-Distance d, then it is necessary to "toggle" d bits to transform one codeword into the other.

The Hamming-Distance of a code is defined as the smallest possible distance two arbitrary (but different) codewords of a code can have.

To detect errors we need a code with valid and invalid words. To be able to detect d bit errors in a codeword the code must have a distance of $d+1$. Why does not less distance suffice?

A code in which toggling one bit turns a codeword into another has the distance 1. Because all codewords are valid there is no redundancy. Every word is allowed. With a distance of 2 at least 2 bits have to be toggled to create a new word. Toggling only one bit instead creates by definition an invalid word. The same applies to more bits.

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Error control: Error correcting codes

Error correcting codes need a distance of 2d+1 if d errors need to be detected and corrected. Why does less distance not suffice?

With a distance of 2d+1 we need to toggle 2d+1 bits to get from one valid codeword to another. If we toggle d bits only then the effort to get back to the original codeword is also exactly toggling d bits. But to get to a different (not the original) codeword when there are d bit errors it is necessary to toggle d+1 bits. If only d bit errors may occur then the shortest (and only) way to get to the next codeword is toggling d bits.

Example: 2 bits are encoded by 10

How many bit errors can be detected and corrected here?

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Error control: Redundancy estimation of error correcting codes

How many bits do we need at least for the correction of a bit error?

We want to have 2^m valid codewords. r correction bits are needed. The error correcting code will have $n = (m + r)$ bits in total. By toggling one of the n bits (redundant bits may be toggled, too) we get an (illegal) codeword with a distance of 1.That means, for each of the 2^m valid words we can create n invalid words. Hence follows

$(n+1)2^m \leq 2^n$

 $(n + 1)$ is composed of n invalid (each created by one bit error) and one valid codeword. The number of words of the error correcting code stands on the right side of the inequality.

n can be written as m data bits plus r correction bits.

 $(m+r+1)2^m \leq 2$ $^{m+r} \Rightarrow$ $(m+r+1) \leq 2^r$

With a given m we can estimate the number of correction bits a code needs in any case.

Example: 8 data bits require 4 correction bits / 1,024 data bits require 11 correction bits Apparently useful especially for larger packets, but only 1-bit errors can be corrected.

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Error control: The error correcting Hamming code

The Hamming code achieves the minimum of the previous estimation.

Algorithm: Number the bits from the LSB to the MSB. All 2ⁿ bits are check bits, the rest are data bits. The data bits are filled up from left to right with the actual data, the check bits only depend on the data bits. Which data bit influences which check bit?

To see this, convert the number of a bit into its binary representation:

 $11 = 8 + 2 + 1$

Data bit 11 hence influences check bits 8, 2 and 1. Check bit 1 is of course also influenced by the data bits 3, 5, 7 and 9. By definition all check bits combined with their data bits have to exhibit an even (or odd, depending what was agreed upon) parity.

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Error control: The error correcting Hamming code

Correction procedure: First, a counter is set to zero. Then, the check bits are checked one by one. If one of them produces the wrong parity, the number of the corresponding check bit is added to the counter. In the end, the counter points to the toggled bit.

Single bit errors can be corrected like this.

Why does the counter value reference the defective bit?

Bit 1 defective? yes: Bits 3,5,7,9 and 11 may be defective Bit 2 defective? no: Bits 5 and 9 are left Bit 4 defective? yes: Only bit 5 is left

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